

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization International Bureau



(43) International Publication Date
17 March 2005 (17.03.2005)

PCT

(10) International Publication Number
WO 2005/024642 A2

(51) International Patent Classification⁷: **G06F 13/38**

(21) International Application Number:
PCT/US2004/029522

(22) International Filing Date:
9 September 2004 (09.09.2004)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/501,894 9 September 2003 (09.09.2003) US

(71) Applicant (for all designated States except US): THOMSON LICENSING S.A. [FR/FR]; 46 Quai A. Le Gallo, F-92100 Boulogne-Billancourt (FR).

(72) Inventors; and

(75) Inventors/Applicants (for US only): TESTIN, William John [US/US]; 6808 Winona Drive, Indianapolis, Indiana 46236 (US). NOVAK, David Gene [US/US]; 17152 Shadoan Way, Westfield, Indiana 46074 (US).

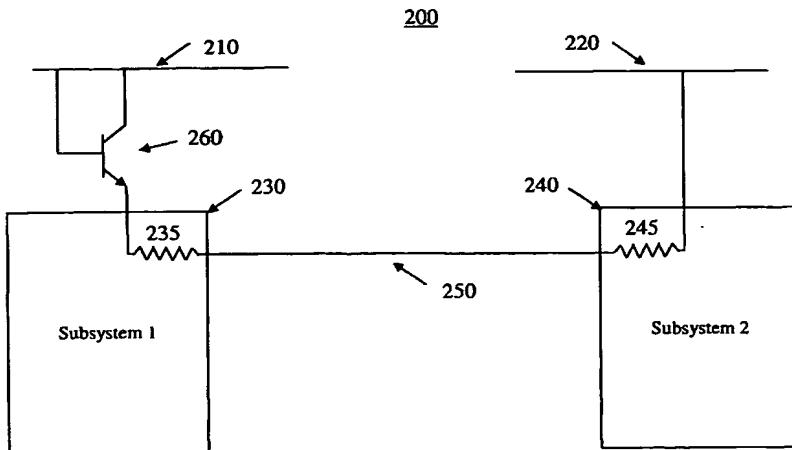
(74) Agents: TRIPOLI, Joseph, S. et al.; c/o THOMSON Licensing Inc., Two Independence Way, Suite 200, Princeton, New Jersey 08540 (US).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

[Continued on next page]

(54) Title: ACTIVE PULL UP APPARATUS FOR A DATA BUS



(57) Abstract: An active pull up configuration for data bus lines unaffected by integral pull up resistors within subsystems. The present application generally relates to digital systems comprising a plurality of power supply levels and data buses. More particularly, this invention relates to digital system comprising subsystems connected by common buses that require automatic charging of certain buses or lines. In a television signal processing apparatus using an I2C bus and using the present invention according to a exemplary embodiment of the present invention, a first device operative in a first mode of operation and a second device operative in said first mode of operation and a second mode of operation wherein said first circuit and said second circuit are both connected by the I2C bus wherein said each data bus line requiring an active pull up is connected to a first power supply via a first resistor integrated within the first device and connected to a second power supply via a second resistor integrated within said second device. The first resistor is electrically isolated from the first power supply during the second mode of operation and electrically connected to the first power supply during the second mode of operation.

WO 2005/024642 A2



Published:

- without international search report and to be republished upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.